

<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		09989777
	Filing Date		2001-11-19
	First Named Inventor	Craig Nemecek	
	Art Unit	2123	
	Examiner Name	Mary C. Jacob	
	Attorney Docket Number	CD01208M	

U.S.PATENTS						
Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	5982229	B1	1999-11-09	Wong et al.	Entire Document
	2	6535946	B1	2003-03-18	Bryant et al.	Entire Document
	3	4827401	B1	1989-05-02	Hrustich et al.	Entire Document
	4	5594734	B1	1997-01-14	Worsley et al.	Entire Document
	5	5781747	B1	1998-07-14	Kametani	Entire Document
	6	6604179	B1	2003-08-05	Volk et al.	Entire Document
	7	6567426	B1	2003-05-20	van Hook et al.	Entire Document
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	11	5557762	B1	1996-09-17	Okuaki et al.	Entire Document
	12	6771552	B1	2004-08-03	Fujisawa	Entire Document
	13	5371860	B1	1994-12-06	Mura et al.	Entire Document
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	18	5630102	B1	1997-05-13	Johnson et al.	Entire Document
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21	5552748	B1	1996-09-03	O'Shaughnessy	Entire Document
22	4689740	B1	1987-08-25	Moelands et al.	Entire Document
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27	5872464	B1	1999-02-16	Gradinariu	Entire Document
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	42	4947169	B1	1990-08-07	Smith et al.	Entire Document
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	47	5440305	B1	1995-08-08	Signore et al.	Entire Document
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**U.S.PATENT APPLICATION PUBLICATIONS**

Examiner Initial*	Cite No	Publication Number	Kind Code <sup>1</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	20020156998	A1	2002-10-24	Casselman, Steven	Entire Document

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**FOREIGN PATENT DOCUMENTS**

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Examiner Initial*	Cite No	Foreign Document Number <sup>3</sup>	Country Code <sup>2</sup>	Kind Code <sup>4</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	T <sup>5</sup>
	1	PCT/US99/09712	WO	A1	1999-02-25	Schutte	Entire Document	<input type="checkbox"/>
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**NON-PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>5</sup>
	1	USPTO Non-Final Rejection for 09/975,115 (CD00182) dated 10/09/08; 34 pages	<input type="checkbox"/>
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6	USPTO Non-Final Rejection for 09/975,115 (CD00182) dated 10/31/06; 19 pages	<input type="checkbox"/>
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9	USPTO Non-Final Rejection for 09/975,115 (CD00182) dated 07/27/05; 11 pages	<input type="checkbox"/>
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	17	USPTO Non-Final Rejection for 09/975,338 (CD00186) dated 04/05/05; 13 pages	<input type="checkbox"/>
	18	Application Number 09/207,912 (CD98086): "Circuit(s), Architecture and Method(s) for Operating and/or Tuning a Ring Oscillator;" Mar; 23 pages 12/9/98	<input type="checkbox"/>
	19	USPTO Non-Final Rejection for 10/113,065 (CD01215M) dated 05/20/2005; 14 pages	<input type="checkbox"/>
	20	USPTO Final Rejection for 10/113,065 (CD01215M) dated 10/26/2005; 17 pages	<input type="checkbox"/>
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	26	USPTO Final Rejection for 09/998,859 (CD01209M) dated 11/19/2003; 5 pages	<input type="checkbox"/>
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28	USPTO Notice of Allowance for 09/998,859 (CD01209M) dated 03/14/2005; 6 pages	<input type="checkbox"/>
29	USPTO Notice of Allowance for 09/975,105 (CD00184) dated 12/04/06; 4 pages	<input type="checkbox"/>
30	USPTO Final Rejection for 09/975,105 (CD00184) dated 07/13/06; 7 pages	<input type="checkbox"/>
31	USPTO Non-Final Rejection for 09/975,105 (CD00184) dated 01/19/06; 5 pages	<input type="checkbox"/>
32	USPTO Non-Final Rejection for 09/975,105 (CD00184) dated 04/19/05; 9 pages	<input type="checkbox"/>
33	Application Number 09/943,062 (CD00228): "Apparatus and Method for Programmable Power Management in a Programmable Analog Circuit Block;" Mar; 46 pages 8/29/01	<input type="checkbox"/>
34	Application Number 10/238,966 (CD01250M): "Method for Parameterizing a User Module;" Perrin; 41 pages 9/9/02	<input type="checkbox"/>
35	USPTO Notice of Allowance for 10/324,455 (CD98087C2) dated 02/12/2004; 4 pages	<input type="checkbox"/>
36	USPTO Non-Final Rejection for 10/324,455 (CD98087C2) dated 11/06/2003; 4 pages	<input type="checkbox"/>
37	USPTO Non-Final Rejection for 10/324,455 (CD98087C2) dated 08/21/2003; 4 pages	<input type="checkbox"/>
38	USPTO Final Rejection for 09/989,778 (CD01172) dated 03/16/09; 26 pages	<input type="checkbox"/>

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39	USPTO Final Rejection for 09/989,765 (CD01194M) dated 03/31/09; 18 pages	<input type="checkbox"/>
40	"In-Circuit Emulators - descriptions of the major ICEs around"; retrieved on November 14, 2005 from <a href="http://www.algonet.se/~staffann/developer/emulator.htm">http://www.algonet.se/~staffann/developer/emulator.htm</a> ; 6 pages	<input type="checkbox"/>
41	CD00184; "Host to FPGA Interface in an In-Circuit Emulation System;" 10/10/01; 09/975,105; Nemecek; 44 pages	<input type="checkbox"/>
42	USPTO Advisory Action for 09/994,601 dated 5/23/06 (CD01170M); 3 pages	<input type="checkbox"/>
43	USPTO Non-Final Rejection for 09/994,601 dated 7/29/04 (CD01170M); 10 pages	<input type="checkbox"/>
44	USPTO Ex Parte Quayle Action for 09/992,076 dated 06/18/07 (CD01210M); 6 pages	<input type="checkbox"/>
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**EXAMINER SIGNATURE**

Examiner Signature	/Mary Jacob/	Date Considered	07/29/2009
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